# VARISPEED-616G5 OPTION CARD PG SPEED CONTROLLER CARD PG-X2 INSTRUCTIONS

Upon receipt of the product and prior to initial operation, read these instructions thoroughly and retain them for future reference.



#### NOTES FOR SAFE OPERATION

Read this instruction manual thoroughly before installation, operation, maintenance or inspection. In this manual, the NOTES FOR SAFE OPERATION is classified as "CAUTION".

## **⚠** CAUTION

Indicates a potentially hazardous situation which, if not avoided, may result in minor or moderate injury to personnel and damage to equipment.

It may also be used to alert against unsafe practices.

Even items described in A CAUTION may result in a vital accident in some situations. In either case, follow these important items.



: These are steps to be taken to insure proper operation and to avoid malfunctions, etc.

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- The option card uses a C MOS IC chip. It may break if touched by bare fingers because of static electricity. Be careful when handling.
- When removing the option card from the inverter for transportation or storage, put the card into the anti-static package it was in when delivered.
- Never change wiring or connect or disconnect connectors while the power is ON.

Failure to observe this caution may injure you.

PG speed controller card, PG-X2 is mounted on the control board of the inverter, and performs speed feedback using the pulse generator (PG) on the motor to correct speed fluctuations caused by slipping. Motor rotation direction is detected by phase A and phase B PG pulse inputs. The card is used for flux vector control.

This option card is applicable to the following inverter series:

VS-616G5: Entire series

Name	Code No.	Functions		
PG speed controller card PG-X2	73600-A015X	<ul> <li>Applicable to RS-422 output PG</li> <li>Phase A and phase B pulse (2-phase pulse) inputs for vector control</li> <li>Maximum input frequencyrange: 50 to 300kHz</li> <li>Pulse monitor output: RS-422 output</li> </ul>		

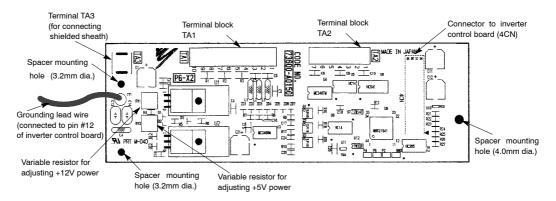


Fig. 1 PG speed controller card PG-X2

Verify that the attachment below is in the package.

• Spacer: 1 pc. (Dimentions in mm)

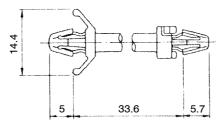


Fig. 2 Spacer (code no. SRNT41028-9)



Before use,

- (1) Before using PG-X2, read this manual and the manual for the installation of the inverter.
- (2) Before connecting PG-X2 or external terminals, turn OFF main power of the inverter and verify the CHARGE indicaor lamp of the inverter is OFF.
- (3) When ordering PG-X2, specify the name and code number.

### 1 Inspection after Delivery

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Verify that ordered products have been delivered.
 Installation of a wrong device may lead to injury or damage.

Though the products have undergone rigorous inspection before shipping, check the following for safety.

- Check the name written on the product to verify that ordered products have been delivered.
- Check for damage caused during transportation.

If there is anything uncertain on the structure, contact your YASKAWA representative.

## 2 Installing to Inverter (See Fig. 3)

#### 2.1 Installation Procedure

- Turn OFF the main power and wait for the time specified on the cover of the inverter. Remove the cover and verify that the CHARGE indicator lamp is OFF.
- ② Insert the attached spacer (SRNT41028–9) into the spacer mounting hole in the mounting base of the inverter. (See Fig. 3.)

Inverters of 3.7kW or smaller capacities have two closely placed holes. Insert the spacer into the hole on the 7CN side. Inserting into the wrong hole will stack the spacer. Be careful to insert in the proper hole in the proper inserting direction.

(3) Align the two holes of PG–X2 and projections as shown in the detailed side view, first at location (a) and then at (b), and precisely place the card on the option A connector. Insert the spacer mounted at (2) above into the PG–X2 spacer mounting hole. (See part A of the side view.)

Verify that 4CN is precisely aligned to PG–X2. Gently push the card until it clicks.

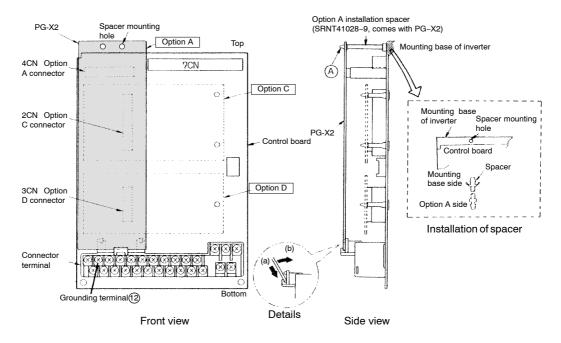


Fig. 3 Installation of PG speed controller card PG-X2

## 3 Interconnection

Fig. 4 shows interconnection between the inverter, PG-X2, and peripheral equipment.

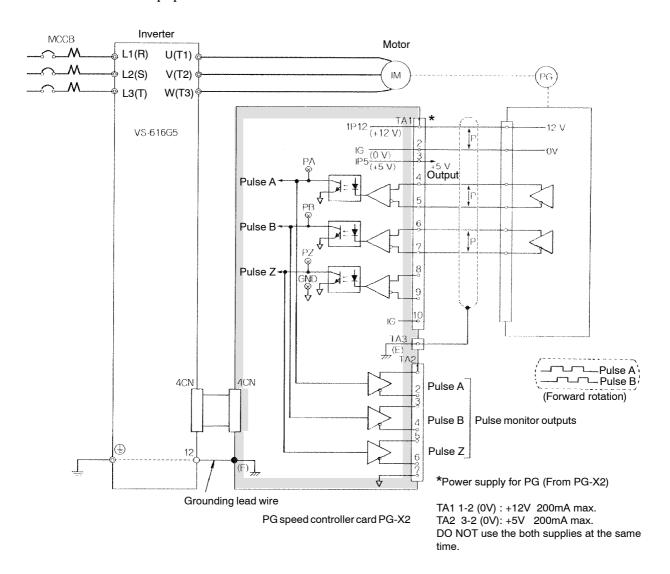


Fig. 4 Interconnection diagram



#### Notes on wiring

- Separate the control signal wires ( from terminals TA1 and TA2 ) of the PG-X2 from the main circuit wires and other power cables.
- Use a shielded wire to connect to the PG. Connect the wire as shown in Fig. 5 to prevent interference by noise. The wire must be 100m or shorter.

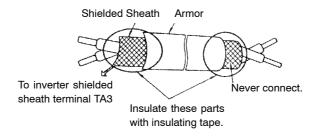


Fig. 5 Shielded wire termination

## 4 Wiring

See Table 1 for the functions of the external terminals.

Table 1 External terminals

Terminal block symbol	Pin No.		Fur	nctions
	1	+12V	For power supply	(+12V, +5V should not be used
	2	0V	+12V 200mA max	at the same time.)
	3	+5V	+5V 200mA max	
	4	+	A mulao	
TA1	5	_	A pulse	
1711	6	+	B pulse	PG signal input* RS-422 level input
	7	_	D puisc	1
	8	+	Z pulse	
	9	_	Z puisc	
	10	0V	Common terminal	
	1	+	A pulse	
	2	_	A pulse	<b>D</b> 1
	3	+	B pulse	Pulse monitor output RS-422 level output
TA2	4	_	D puise	1
	5	+	Z pulse	
	6	-	2 puise	
	7	SG	Common terminal	
TA3			Shielded sheath cor	nnection terminal

<sup>\*</sup>For two-phase pulse input from PG, input 90° lead pulse to A-pulse side and 90° delay pulse to B-pulse side during forward run.

#### PG signal output

The PG signal output (phases A and B) may vary according to installation location on the motor. Refer to Fig. 4 for correct wiring.

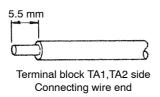
In general, motor forward direction is counterclockwise (CCW) as viewed from the load shaft. For YASKAWA's motor, phase A of PG output leads phase B by a phase angle of 90° in clockwise (CW) rotation. According to PG, phase A lags phase B by a phase angle of 90° in clockwise (CW) rotation. In this case, when PG is installed at the opposite drive end, connect phases A and B output from PG to the option card as it is.

For YASKAWA's inverter motor with PG, PG is installed at the opposite drive end. Then, phase A lags phase B by a phase angle of 90° at motor forward run. (Motor runs CCW as viewed from PG.) Therefore, when using this motor or similar motors, connect phases A and B to the option card after replacing phase output. The pulse monitor on this option shows phase A leading phase B by a phase angle of 90°.

Make sure the followings when wiring.

- To prevent noise, use shielded wire and separate from heavy current circuits (200VAC or greater) or relay drive circuits. (Wire length to the PG connector must be 50m or shorter.)
- Connect both ends of the unused wire of the shielded wire to the 0V terminal.
- Connect the grounding lead wire (E) to pin (12) of the control board of the inverter
- Applicable wire sizes for terminal block TA1 and TA2 are shown below.
   [Terminal: MKDS1 series manufactured by Phoenix Contact GmbH & Co.]

	[mm <sup>2</sup> ]	AWG	I [A]	VAC [V]
Thin twisted wire	1	16	12	125
Solid wire	1.5	16	12	125
UL	_	22–16	10	300
CSA	_	28-16	10	300
CSA	_	28-16	10	150





Notes on selecting cables

Too thick a cable applies pressure to the option card and may lead to failure. Too thin a cable may lead to imperfect contact or a break in the wire.

## 5 Selecting PG

The maximum frequency of PG output pulse that can be detected is 300kHz. Find output frequency,  $f_{PG}(Hz)$ , according to the following formula.

$$f_{PG}\left(Hz\right) \ = \frac{Motor\ rotation\ speed\ (r/min)\ at\ max.\ frequency\ output}{60} \times\ PG\ constant\ (p/rev)$$

If the PG power capacity is 200mA or greater, provide a separate power supply. (If momentary power loss ride-through function is necessary, provide backup capacitor or take other necessary measures.)

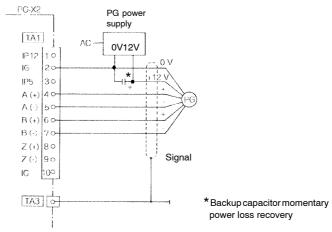


Fig. 6 Connection example with PG when using separate power supply

Table 2 Constants List

Digital Operator	Digital Operator	Con-		Setting		Change during			Control Method (O=Setting enable x=Setting disable)	Method g enable g disable)	
Function Display	Display	stant No.	Constant Name	Range	Factory Setting	Operation (O=Enable, x=Disable)	Data Selection	V/f Control	V/f with PG Feed- back	Open Loop Vector	Flux Vector
	PG Pulse/Rev	F1-01	PG constant	0 to 60000	1624 *3	×		×	0	×	0
	PG Fdbk Loss Sel	F1-02	Operation selection at PG open circuit	0 to 3	-	×	0 : Ramp to Stop 1 : Coast to Stop 2 : Fast-Stop 3 : Alarm Only	×	0	×	0
	PG Overspeed Sel	F1-03	Operation selection at overspeed	0 to 3	-	×	0 : Ramp to Stop 1 : Coast to Stop 2 : Fast-Stop 3 : Alarm Only	×	0	×	0
	PG Deviation Sel	F1-04	Operation selection at deviation	0 to 3	n	×	0 : Ramp to Stop 1 : Coast to Stop 2 : Fast-Stop 3 : Alarm Only	×	0	×	0
PG Option Setup	PG Rotation Sel	F1-05	PG rotation	0/1	0	×	0 : Fwd=C.C.W. 1 : Fwd=C.W.	×	0	×	0
	PG Output Ratio	F1-06	PG division rate	1 to 132	1	×		×	0	×	0
	PG Ramp PI/I Sel	F1-07	Integral value during accel/decel enable/disable	0/1	0	×	0 : Disabled 1 : Enabled	×	0	×	×
	PG Overspd Level	F1-08	PG overspeed detection level	0 to 120%	115%	×		×	0	×	0
	PG Overspd Time	F1-09	PG overspeed detection delay time	0 to 2.0s	0.0s *1	×		×	0	×	0
	PG Deviate Level	F1-10	Excessive speed deviation detection level	0 to 50%	10%	×		×	0	×	0
	PG Deviate Time	F1-11	Excessive speed deviation detection delay time	0 to 2.0s	0.5s	×		×	0	×	0
	PG# Gear Teeth 1	F1-12	Number of PG gear teeth 1	0 to 1000	0	×		×	0	×	×
	PG # Gear Teeth 2	F1-13	Number of PG gear teeth 2	0 to 1000	0	×		×	0	×	×
	PGO Detect time *4	F1-14	PG open circuit detection delay time	0.0 to 10,0	2.0s	×		×	0	×	0
	ASR P Gain 1	C5-01	ASR proportional gain 1	0 to 300.00	$20.00^{*1} (0.00)^{*2}$	0		×	0	×	0
	ASR I Time 1	C5-02	ASR integral time 1	0 to 10.000s		0		×	0	×	0
	ASR P Gain 2	C2-03	ASR proportional gain 2	0 to 300.00	$20.00^{*1} (0.20)^{*2}$	0		×	0	×	0
,	ASR I Time 2	C5-04	ASR integral time 2	0 to 10.000s	0.500s *1 (1.000s) *2	0		×	0	×	0
ASR Tuning*5	ASR Limit	C5-05	ASR limit	0.0 to 20.0%	5.0% *2	×		×	0	×	×
	ASR Delay Time	C5-06	ASR primary delay time	0.000 to 0.500s	0.004s *1	×		×	×	×	0
	ASR Gain SW Freq	C5-07	ASR switching frequency	0.0 to 400.0Hz	0.0Hz	×		×	×	×	0
	*1 Flux vector control	ctor cor	ntrol *2 V/f with PG feedback control	3 feedback c	ontrol		*3 Value when o2-09=1 or		2		

\*1 Flux vector control \*2 V/f with PG feedback control \*4 Setting and reference are enabled for the software No. (U1–14) of 0130 or after.

<sup>\*5</sup> ASR = Automatic Speed Regulation